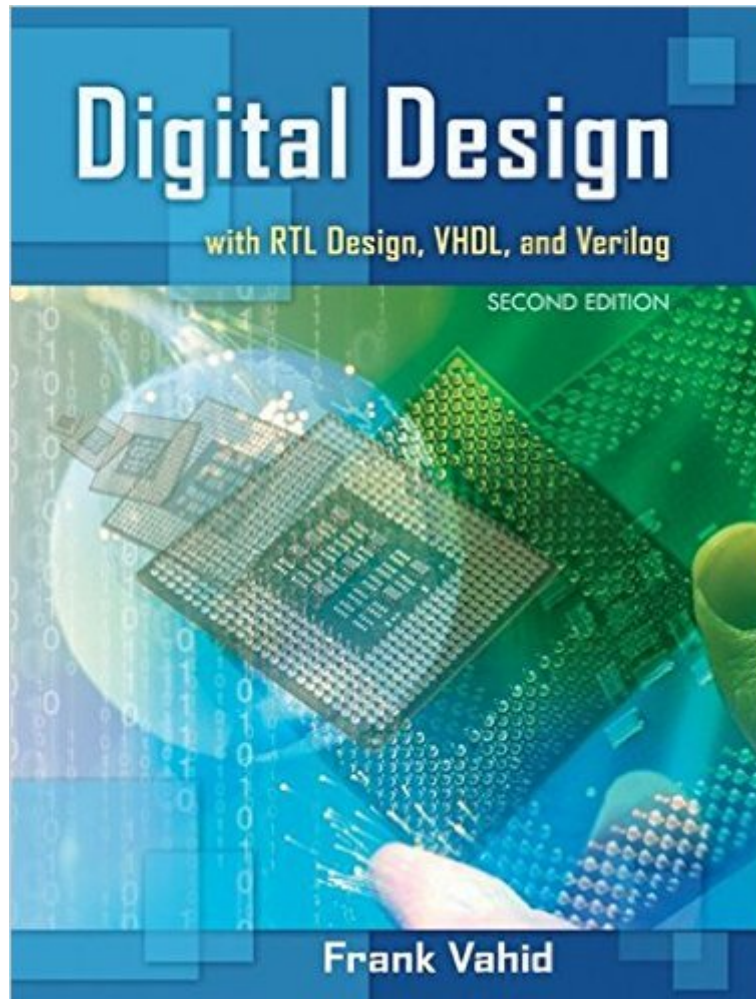


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# Digital Design With RTL Design, VHDL, And Verilog



## Synopsis

An eagerly anticipated, up-to-date guide to essential digital design fundamentals Offering a modern, updated approach to digital design, this much-needed book reviews basic design fundamentals before diving into specific details of design optimization. You begin with an examination of the low-levels of design, noting a clear distinction between design and gate-level minimization. The author then progresses to the key uses of digital design today, and how it is used to build high-performance alternatives to software. Offers a fresh, up-to-date approach to digital design, whereas most literature available is sorely outdated Progresses through low levels of design, making a clear distinction between design and gate-level minimization Addresses the various uses of digital design today Enables you to gain a clearer understanding of applying digital design to your life With this book by your side, you'll gain a better understanding of how to apply the material in the book to real-world scenarios.

## Book Information

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## Customer Reviews

This is a required textbook for our Digital Circuits class, over all I think the book is very simple and easy to understand. It's not written for people who are geniuses, it's actually written pretty down to earth. The only problem is that the book lacks depth, it doesn't go into very much details. I would be looking for problems as tough as my class assigns, but the book itself doesn't teach that depth.

Textbook was alright, I only bought it because my teacher said it would really help us. I definitely

could have completed the course without the help of this textbook. I only used it a couple times. Most of the stuff in this textbook is easily accessible on the internet. Its not a very modern textbook.

This book was given to us for our Intro to Digital Design and Circuits class. I personally loved reading the book as a learning enthusiast and an ECE major. The book provides good examples that relate to real life to help you understand. I still keep the book to this day as a reference. I recommend the book to anyone starting out, not necessarily for people who already know their basics.

Needed this book for my class and got it in time. The book itself is a great help to gain insight into digital design and logic but it is too wordy for me. The author goes on and on and on and.... just useless amount of rant to explain something so elementary and easy. At times I agree with the need for such technical jargon but most of the time the wordiness is a turn off. Very traditional and old world style text book writing where most of the book is actual TEXT. The examples however are a bit more helpful. Too bad there aren't enough of them.

I paid a lot of money for this book. It's nice, well written, but only a rehash of what I already know after 25 years of digital chip design. I was hoping that what calls it "Digital System Design with SystemVerilog" would be what I received. I should have looked at the "look inside" section more closely. Check out the title of the book - "Digital Design with RTL Design, VHDL, and Verilog" - no mention of SystemVerilog! I'm still keeping it because it's a good reference and it's a lot newer than my Palnitkar and Bhasker Verilog books. I really think I got baited and switched by the description. Nonetheless, if you're a student or have less than 10 years experience coding RTL, I think this book would be good. I also ordered SystemVerilog for Design and Verilog and SystemVerilog Gotchas by Sutherland et al and find those to be much more what I needed. I find that in general, while the Springer publisher books are very expensive and generally small, they are a lot more directed to experienced designers. I don't work for Springer and I don't know Sutherland so I hope you believe this is a sincere review. I just don't want other people throwing away money on something they already know.

Absolutely pristine condition. No marks, scratches, corner bumps, etc. Great product, Delivered on time and was exactly what was shown. Although my professor hardly used the book during lectures, came in handy when teaching myself beginning VHDL coding.

If you're taking EECS31(intro to digital systems) at a U.C. this is the correct book

great book for beginners like me

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